

S/N 09/917,257



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Dmitriy Rumynin et al.	Examiner:	David H. Malzahn
Serial No.:	09/917,257	Group Art Unit:	2124
Filed:	July 27, 2001	Docket:	1365.051US1
Title:	A PARALLEL COUNTER AND A LOGIC CIRCUIT FOR PERFORMING MULTIPLICATION		

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Please charge the fee of \$180.00 to Deposit Account No. 19-0743.

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No :09/917,257

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Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

DMITRIY RUMYNIN ET AL.


By their Representatives,

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Date

23 March '05

By

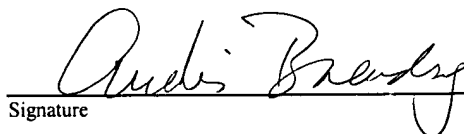
  
Timothy B Clise  
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 23 day of March, 2005.

CANDIS BUENDING

Name

Signature



Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE                  STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)	Complete if Known	
	Application Number	09/917,257
	Filing Date	July 27, 2001
	First Named Inventor	Rumynin, Dmitriy
	Group Art Unit	2124
	Examiner Name	Malzahn, David
Sheet 1 of 1	Attorney Docket No: 1365.051US1	

US PATENT DOCUMENTS				
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-2004/0103135A1	05/27/2004	Talwar, S.	09/22/2003
	US-2004/0153490A1	08/05/2004	Talwar, S. , et al.	11/14/2003
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	US-5,701,504	12/23/1997	Timko, M. A.	12/28/1994

FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T <sup>2</sup>
	GB-2263002	07/07/1993	Poon, J. T.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-Issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
		NICHOLSON, J. O., "Parallel-Carry Adders Listing Two-Bit Covers", <u>IBM Technical Disclosure Bulletin</u> , 22(11), (April, 1980),5036-5037		
		ONG, S. , et al., "A Comparision of ALU Structures for VLSI Technology", <u>Proceedings, 6th Symposium on Computer Arithmetic (IEEE)</u> , (1983),10-16		
		SCHMOOKLER, M. S., et al., "Group-Carry Generator", <u>IBM Technical Disclosure Bulletin</u> , 6(1), (June, 1963),77-78		
		WEINBERGER, A. , "Extension of the Size of Group Carry Signals", <u>IBM Technical Disclosure Bulletin</u> , 22(4), (September, 1979),1548-1550		
		WEINBERGER, A. , "Improved Carry-Look-Ahead", <u>IBM Technical Disclosure Bulletin</u> , 21(6), (November, 1978),2460-2461		

EXAMINER

DATE CONSIDERED